

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Airoyaye
Attorney Docket Number	CD00232M

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	6825689	B1	2004-11-30	Snyder	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Sullam	Entire Document
	5	6967511	B1	2005-11-22	Sullam	Entire Document
	6	6892322	B1	2005-05-10	Snyder	Entire Document
	7	6823282	B1	2004-11-23	Snyder	Entire Document
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Attorney Docket Number	CD00232M

9	6611220	B1	2003-08-26	Snyder	Entire Document
10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
11	6765407	B1	2004-07-20	Snyder	Entire Document
12	7386740	B1	2008-06-10	Kutz et al.	Entire Document
13	6952778	B1	2005-10-04	Snyder	Entire Document
14	7185162	B1	2007-02-27	Snyder	Entire Document
15	6854067	B1	2005-02-08	Kutz et al.	Entire Document
16	6614320	B1	2003-09-02	Sullam et al.	Entire Document
17	6868500	B1	2005-03-15	Kutz et al.	Entire Document
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20	6981090	B1	2005-12-27	Kutz et al.	Entire Document
21	6603330	B1	2003-08-05	Snyder	Entire Document
22	6507214	B1	2003-01-14	Snyder	Entire Document
23	7005933	B1	2006-02-28	Shutt	Entire Document
24	6542025	B1	2003-04-01	Kutz et al.	Entire Document
25	6892310	B1	2005-05-10	Kutz et al.	Entire Document
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29	6950954	B1	2005-09-27	Suliam et al.	Entire Document
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31	6563391	B1	2003-05-13	Mar	Entire Document
32	6608472	B1	2003-08-19	Kutz et al.	Entire Document
33	7185321	B1	2007-02-27	Roe et al.	Entire Document

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NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>

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2	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 06/30/08; 12 pages	<input type="checkbox"/>
3	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 12/26/07; 12 pages	<input type="checkbox"/>
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5	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/19/07; 7 pages	<input type="checkbox"/>
6	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 10/20/06; 8 pages	<input type="checkbox"/>
7	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/06/06; 8 pages	<input type="checkbox"/>
8	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al.; 25 pages	<input type="checkbox"/>
9	Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Mar; 28 pages	<input type="checkbox"/>
10	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance," Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
11	"WP 3.5: An Integrated Time Reference," Blauschild, Digest of Technical Papers, 1994; 4 pages	<input type="checkbox"/>
12	"Micropower CMOS Temperature Sensor with Digital Output," Bakker et al., IEEE Journal of Solid-State Circuits, 1996; 3 pages	<input type="checkbox"/>

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13	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 05/11/05; 10 pages	<input type="checkbox"/>
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15	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 03/02/06; 14 pages	<input type="checkbox"/>
16	USPTO Final Rejection for 10/001,477 (CD01203) dated 08/24/06; 13 pages	<input type="checkbox"/>
17	USPTO Non-Final Rejection for 10/001,478 (CD01213) dated 10/20/08; 18 pages	<input type="checkbox"/>
18	USPTO Final Rejection for 10/001,478 (CD01213) dated 06/04/08; 18 pages	<input type="checkbox"/>
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21	USPTO Non-Final Rejection for 10/001,478 (CD01213) dated 04/02/07; 17 pages	<input type="checkbox"/>
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24	USPTO Final Rejection for 10/001,478 (CD01213) dated 10/24/05; 15 pages	<input type="checkbox"/>
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31	USPTO Final Rejection for 10/002,217 (CD01207) dated 03/07/07; 12 pages	<input type="checkbox"/>
32	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 10/02/06; 21 pages	<input type="checkbox"/>
33	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 04/03/06; 12 pages	<input type="checkbox"/>
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35	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 05/19/05; 15 pages	<input type="checkbox"/>
36	USPTO Notice of Allowance for 10/001,477 (CD01203) dated 11/10/08; 7 pages	<input type="checkbox"/>
37	USPTO Advisory Action for 10/001,477 (CD01203) dated 10/10/08; 3 pages	<input type="checkbox"/>
38	USPTO Final Rejection for 10/001,477 (CD01203) dated 06/30/08; 15 pages	<input type="checkbox"/>
39	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 12/06/07; 13 pages	<input type="checkbox"/>
40	USPTO Final Rejection for 10/001,477 (CD01203) dated 07/23/07; 16 pages	<input type="checkbox"/>
41	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 01/22/07; 15 pages	<input type="checkbox"/>

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EXAMINER SIGNATURE

Examiner Signature	Date Considered
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Doc code: IDS

PTO/SB/08a (03-09)

Doc description: Information Disclosure Statement (IDS) Filed

Approved for use through 04/30/2009. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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U.S.PATENTS

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	1	5982229	B1	1999-11-09	Wong et al.	Entire Document
	2	6535948	B1	2003-03-18	Bryant et al.	Entire Document
	3	4827401	B1	1989-05-02	Hrustich et al.	Entire Document
	4	5594734	B1	1997-01-14	Worsley et al.	Entire Document
	5	5781747	B1	1998-07-14	Kametani	Entire Document
	6	6604179	B1	2003-08-05	Volk et al.	Entire Document
	7	6567426	B1	2003-05-20	van Hook et al.	Entire Document
	8	5430687	B1	1995-07-04	Hung et al.	Entire Document

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9	6249167	B1	2001-06-19	Oguchi et al.	Entire Document
10	4134073	B1	1979-01-01	MacGregor	Entire Document
11	5557762	B1	1996-09-17	Okuaki et al.	Entire Document
12	6771552	B1	2004-08-03	Fujisawa	Entire Document
13	5371860	B1	1994-12-06	Mura et al.	Entire Document
14	6575373	B1	2003-06-10	Nakano	Entire Document
15	5710906	B1	1998-01-20	Ghosh et al.	Entire Document
16	6294962	B1	2001-09-25	Mar	Entire Document
17	5590354	B1	1996-12-31	Klapproth et al.	Entire Document
18	5630102	B1	1997-05-13	Johnson et al.	Entire Document
19	5574892	B1	1996-11-12	Christensen	Entire Document

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20	5331571	B1	1994-07-19	Aronoff et al.	Entire Document
21	5552748	B1	1996-09-03	O'Shaughnessy	Entire Document
22	4689740	B1	1987-08-25	Moelands et al.	Entire Document
23	5559502	B1	1996-09-24	Schutte	Entire Document
24	5689196	B1	1997-11-18	Schutte	Entire Document
25	5670915	B1	1997-09-23	Cooper et al.	Entire Document
26	5684434	B1	1997-11-04	Mann et al.	Entire Document
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31	5428319	B1	1995-06-27	Marvin et al.	Entire Document
32	5604466	B1	1997-02-18	Dreps et al.	Entire Document
33	5898345	B1	1999-04-27	Namura et al.	Entire Document
34	5304955	B1	1994-04-19	Atriss et al.	Entire Document
35	5870345	B1	1999-02-09	Stecker	Entire Document
36	5870004	B1	1999-02-09	Lu	Entire Document
37	5699024	B1	1997-12-16	Manlove et al.	Entire Document
38	5345195	B1	1994-09-06	Cordoba et al.	Entire Document
39	4272760	B1	1981-06-09	Prazak et al.	Entire Document
40	4344067	B1	1982-08-10	Lee	Entire Document
41	4868525	B1	1989-09-19	Dias	Entire Document

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42	4947169	B1	1990-08-07	Smith et al.	Entire Document
43	5140197	B1	1992-08-18	Grider	Entire Document
44	5150079	B1	1992-09-22	Williams et al.	Entire Document
45	5200751	B1	1993-04-06	Smith	Entire Document
46	5319370	B1	1994-06-07	Signore et al.	Entire Document
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	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
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6	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/31/06; 19 pages	<input type="checkbox"/>
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33	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block;" Mar; 46 pages	<input type="checkbox"/>
34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module;" Perrin; 41 pages	<input type="checkbox"/>
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	Filing Date		2001-10-22
	First Named Inventor	Warren Snyder	
	Art Unit	2183	
	Examiner Name	Idriss N. Alrobaye	
	Attorney Docket Number	CD00232M	

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12	CD00185; "Emulator Chip-Board Architecture for Interface;" October 10, 2001; 09/975,030; Snyder et al.; 37 pages	<input type="checkbox"/>
13	Wikipedia - Main Page, retrieved on March 8, 2006 from http://en.wikipedia.org/wiki/Main_Page and http://en.wikipedia.org/wiki/Wikipedia:Introduction ; 5 pages	<input type="checkbox"/>
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Application Number	10033027
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First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

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27	USPTO Final Rejection for 09/989,778 (CD01172M) dated 02/05/07; 17 pages	<input type="checkbox"/>
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	1	7188063	B1	2007-03-06	Snyder, Warren	Entire Document
	2	6002398	B1	1999-12-14	Wilson, Daniel	Entire Document
	3	6014135	B1	2000-01-11	Fernandes, Antonio	Entire Document
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10	6384947	B1	2002-05-07	Ackerman, et al.	Entire Document
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21	5874958	B1	1999-02-23	Ludolph, Frank	Entire Document
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	2	6785881	B1	2004-08-31	Bartz et al.	Entire Document
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15	Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm;" Snyder; 26 pages	<input type="checkbox"/>
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17	Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture;" Snyder; 36 pages	<input type="checkbox"/>
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	1	"New Object Domain R3 Beta Now Available (Build 134)!" March 13, 2001; < http://web.archive.org/web/200100331202605/www.objectdomain.com/domain30/index.html >; 2 pages	<input type="checkbox"/>
	2	"OMG XML Metadata Interchange (XMI) Specifications" 2000; 17 pages	<input type="checkbox"/>
	3	Electronic Tools Company; E-Studio User Manuel; 2000; retrieved from http://web.archive.org for site http://e-tools.com on March 23, 2005; 77 pages	<input type="checkbox"/>
	4	Cover Pages Technology Reports; XML and Electronic Design Automation (EDA); August 2000; retrieved from http://xml.coverpages.org on March 23, 2005; 5 pages	<input type="checkbox"/>
	5	Microsoft Computer Dictionary "ActiveX" 2002; Microsoft Press; 5th Edition; 3 pages	<input type="checkbox"/>
	6	Wikipedia "XML" retrieved on January 29, 2007 from http://en.wikipedia.org/wiki/XML ; 16 pages	<input type="checkbox"/>

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Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

7	"VHDL Samples" retrieved on January 29, 2007 from http://www.csee.umbc.edu/help/VHDL/samples/samples.shtml ; 10 pages	<input type="checkbox"/>
8	Anonymous, "Lotus Notes FAQ -- How do you generate unique document numbers?" September 19, 1999; retrieved from www.keysolutions.com on July 9, 2008; 1 page	<input type="checkbox"/>
9	Ashok Bindra, "Programmable SoC Delivers A New Level Of System Flexibility"; Electronic Design; November 6, 2000; 11 pages	<input type="checkbox"/>
10	Cypress MicroSystem, Inc. "Cypres Customer Forums" retrieved from http://www.cypress.com/forums/messageview ; 1 page	<input type="checkbox"/>
11	Cypress MicroSystem, Inc. "PsoC Designer: Integrated Development Environment User Guide"; Rev. 1.18; 9/8/2003; 193 pages	<input type="checkbox"/>
12	Hamblen, "Rapid Prototyping Using Field-Programmable Logic Devices" June 2000, IEEE; 9 pages	<input type="checkbox"/>
13	USPTO Final Rejection for Application Number 09/943,062 (CD00228) dated 04/30/2004; 9 pages	<input type="checkbox"/>
14	Snyder et al., "Xilinx's A-to-Z Systems Platform" Cahners Microprocessor, The Insider's Guide to Microprocessor Hardware, February 6, 2001; 6 pages	<input type="checkbox"/>
15	"PSoC Technology Completely Changes 8-bit MCU System Design" Cypress MicroSystem, Inc. February 19, 2001; 21 pages	<input type="checkbox"/>
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20	USPTO Non-Final Rejection for Application Number 09/943,062 (CD00228) dated 09/11/2002; 9 pages	<input type="checkbox"/>
21	"PSoC Designer: Integrated Development Environment" User Guide; Revision 1.11; Last Revised 07/17/2001; 109 pages	<input type="checkbox"/>
22	Cypress Microsystems, "Cypress Microsystems Unveils Programmable System-on-a-Chip for Embedded Internet, Communications and Consumer Systems;" 2000, < http://www.cypressmicro.com/corporate/CY_Announces_nov_13_2000.html >; 3 pages	<input type="checkbox"/>
23	Huang et al., "ICEBERG, An Embedded In-Circuit Emulator Synthesizer for Microcontrollers, Proceedings of the 36th Design Automation Conference 1999; 6 pages	<input type="checkbox"/>
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26	USPTO Final Rejection for Application Number 09/943,062 (CD00228) dated 01/18/2008; 8 pages	<input type="checkbox"/>
27	USPTO Non-Final Rejection for Application Number 09/943,062 (CD00228) dated 06/22/2007; 12 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6714066	B1	2004-03-30	Gorecki et al.	Entire Document
	2	6453175	B1	2002-09-17	Mizell et al.	Entire Document
	3	6185127	B1	2001-02-06	Myers et al.	Entire Document
	4	6141376	B1	2000-10-31	Shaw	Entire Document
	5	5905398	B1	1999-05-18	Todsen et al.	Entire Document
	6	5574678	B1	1996-11-12	Gorecki	Entire Document
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	8	6011407	B1	2000-01-04	New	Entire Document

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10	5396245	B1	1995-03-07	Rempfer	Entire Document
11	3940760	B1	1976-02-01	Brokaw	Entire Document
12	5963075	B1	1999-10-05	Hiiragizawa	Entire Document
13	6150866	B1	2000-11-21	Eto et al.	Entire Document
14	5307381	B1	1994-04-26	Ahuja	Entire Document
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20	6728900	B1	2004-04-27	Meli	Entire Document
21	5544311	B1	1996-08-06	Harenberg et al.	Entire Document
22	5664199	B1	1997-09-02	Kuwahara	Entire Document
23	6175914	B1	2001-01-16	Mann	Entire Document
24	6574590	B1	2003-06-03	Kershaw et al.	Entire Document
25	5479652	B1	1995-12-26	Dreyer et al.	Entire Document
26	6356960	B1	2002-03-12	Jones et al.	Entire Document
27	6101457	B1	2000-08-08	Barch et al.	Entire Document
28	6125416	B1	2000-09-26	Warren	Entire Document
29	6681359	B1	2004-01-20	Au et al.	Entire Document
30	6185732	B1	2001-02-06	Mann et al.	Entire Document

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31	6041406	B1	2000-03-21	Mann	Entire Document
32	5088822	B1	1992-02-18	Warren	Entire Document
33	5935266	B1	1999-08-10	Thurnhofer et al.	Entire Document
34	6463488	B1	2002-10-08	San Juan	Entire Document
35	5053949	B1	1991-10-01	Allison et al.	Entire Document
36	6026501	B1	2000-02-15	Hohl et al.	Entire Document
37	6567932	B1	2003-05-20	Edwards et al.	Entire Document
38	6591369	B1	2003-07-08	Edwards et al.	Entire Document
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43	5663965	B1	1997-09-02	Seymour	Entire Document
44	6191603	B1	2001-02-20	Muradali et al.	Entire Document
45	6005814	B1	1999-12-21	Mulholland et al.	Entire Document
46	5802073	B1	1998-09-01	Platt	Entire Document
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48	6061511	B1	2000-05-09	Martantz et al.	Entire Document
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Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

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	2	6051772	B1	2000-04-18	Cameron et al.	Entire Document
	3	6016554	B1	2000-01-18	Skrovan et al.	Entire Document
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9	6396302	B1	2002-05-28	New et al.	Entire Document
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38	Application Number 09/275,336 (CD98087): "Programmable Oscillator Scheme;" Mar et al.; 25 pages	<input type="checkbox"/>
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Application Number	10033027
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First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

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	1	Application Number 11/850,260 (CD2007006): "Circuit and Method for Improving the Accuracy of a Crystal-less Oscillator Having Dual-Frequency Modes;" Wright et al.; 33 pages	<input type="checkbox"/>
	2	USPTO Non-Final Rejection for Application Number 11/850,260 (CD2007006) dated 03/06/2009; 7 pages	<input type="checkbox"/>
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6	USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 04/14/2008; 10 pages	<input type="checkbox"/>
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21	USPTO Non-Final Rejection for Application Number 11/322,044 (CD05198) dated 04/11/2008; 11 pages	<input type="checkbox"/>
22	USPTO Advisory Action for Application Number 11/322,044 (CD05198) dated 11/30/2007; 2 pages	<input type="checkbox"/>
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	1	Application Number 10/226,911 (CD02112): "Calibration of Integrated Circuit Time Constants," Gehring et al.; 32 pages	<input type="checkbox"/>
	2	USPTO Notice of Allowance for Application Number 09/943,149 (CD01085) dated 01/12/2004; 7 pages	<input type="checkbox"/>
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	4	USPTO Final Rejection for Application Number 09/943,149 (CD01085) dated 05/07/2003; 10 pages	<input type="checkbox"/>
	5	USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 11/20/2002; 7 pages	<input type="checkbox"/>
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	7	Durham et al., "Integrated Continuous-Time Balanced Filters for 16-bit DSP Interfaces," IEEE, 1993; 6 pages	<input type="checkbox"/>

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

8	Durham et al., "Circuit Architectures for High Linearity Monolithic Continuous-Time Filtering," IEEE, 1992; 7 pages	<input type="checkbox"/>
9	Durham et al., "High-Linearity Conitnuous-Time Filter in 5-V VLSI CMOS," IEEE, 1992; 8 pages	<input type="checkbox"/>
10	Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Scott; 35 pages	<input type="checkbox"/>
11	Application Number 09/216,460 (CD98099): "Circuit and Method for Controlling an Output of a Ring Oscillator," Abugharbieh et al.; 21 pages	<input type="checkbox"/>
12	Application Number 09/471,914 (CD99064): "Reference-Free Clock Generator and Data Recovery PLL," Dalmia et al.; 32 pages	<input type="checkbox"/>
13	Application Number 09/471,576 (CD99062): "Reference-Free Clock Generation and Data Recovery PLL," Dalmia; 30 pages	<input type="checkbox"/>
14	Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range," Meyers et al.; 28 pages	<input type="checkbox"/>
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16	Application Number 09/893,161 (CD00094): "Architecture of a PLL with Dynamic Frequency Control on a PLD," Moore; 32 pages	<input type="checkbox"/>
17	Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al.; 24 pages	<input type="checkbox"/>
18	Application Number 09/398,956 (CD99049): "Frequency Acquisition Rate Control in Phase Lock Loop Circuits," Moyal et al.; 35 pages	<input type="checkbox"/>

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19	Application Number 09/747,262 (CD00085): "Linearized Digital Phase-Locked Loop," Williams et al.; 9 pages	<input type="checkbox"/>
20	Application Number 09/981,448 (CD01102): "Oscillator Tuning Method," Hauck; 28 pages	<input type="checkbox"/>
21	Application Number 09/538,989 (CD98104): "Memory Based Phase Locked Loop," Krishnan; 27 pages	<input type="checkbox"/>
22	Application Number 09/048,905 (CD95045): "Programmable Clock Generator," Mann et al.; 42 pages	<input type="checkbox"/>
23	Application Number 08/865,342 (CD95045C): "Programmable Clock Generator," Mann et al.; 41 pages	<input type="checkbox"/>
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25	USPTO Non-Final Rejection for Application Number 10/293,392 (CD02135) dated 10/16/2003; 6 pages	<input type="checkbox"/>
26	Application Number 10/293,392 (CD02135): "Low Voltage Receiver Circuit and Method for Shifting the Differential Input Signals of the Receiver Depending on a Common Mode Voltage of the Input Signals," Maher et al.; 20 pages	<input type="checkbox"/>
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28	USPTO Final Rejection for Application Number 10/288,003 (CD02108) dated 10/06/2004; 6 pages	<input type="checkbox"/>
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	1	6411665	B1	2002-06-25	Chan et al.	Entire Document
	2	5867046	B1	1999-02-02	Sugasawa, Yasuo	Entire Document
	3	5517198	B1	1996-05-14	McEwan, Thomas E.	Entire Document
	4	5361290	B1	1994-11-01	Akiyama, Shin-ichiro	Entire Document
	5	6768352	B1	2004-07-27	Maher et al.	Entire Document
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9	5392784	B1	1995-02-28	Gudaitis	Entire Document
10	6292028	B1	2001-09-18	Tomita	Entire Document
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21	4809345	B1	1989-02-28	Tabata et al.	Entire Document
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31	5334952	B1	1994-08-02	Maddy et al.	Entire Document
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42	5043674	B1	1991-08-27	Bonaccio et al.	Entire Document
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49	6294932	B1	2001-09-25	Watarai, Seiichi	Entire Document
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1	20030135842	A1	2003-07-17	Frey et al.	Entire Document
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17	Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming," Bartz et al.; 67 pages	<input type="checkbox"/>
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28	USPTO Final Rejection for Application Number 09/989,761 (CD01179M) dated 08/26/2004; 7 pages	<input type="checkbox"/>
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39	PCT International Search Report of the International Searching Authority for PCT/US05/28793, filed 08/12/2005, mailed 11/19/2007; 5 pages	<input type="checkbox"/>
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	3	4942540	B1	1990-07-17	Black et al.	Entire Document
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	1	Kory Hopkins, "Definition;" 01/16/1997; < http://www.cs.sfu.ca/cs/people/GradStudent.html >; 1 page	<input type="checkbox"/>
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	3	"The Gemini Netlist Comparison Project;" < http://www.cs.washington.edu/research/projects/lis/www/gemini/gemini.html > larry@cs.washington.edu; 2 pages	<input type="checkbox"/>
	4	Ohlrich et al., "Sub-Gemini: Identifying Subcircuits using a Fast Subgraph Isomorphism Algorithm;" June 1993; in proceedings of the 30th IEEE/ACM Design Automation Conference; 7 pages	<input type="checkbox"/>
	5	Ebling, "Gemini II: A Second Generation Layout Validation Program;" 1988; in proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-88); 4 pages	<input type="checkbox"/>
	6	Application Number 12/132,527 (CD01175C): "System and Method for Performing Next Placements and Pruning of Disallowed Placements for Programming an Integrated Circuit;" Ogami et al.; 44 pages	<input type="checkbox"/>
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